



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Richard L. Coulson et al.	§	Art Unit:	2863
Serial No.:	§	Examiner:	Aditya S. Bhat
Filed:	§	Docket:	ITL.1029US P16765
For: Adaptive Cache Algorithm for Temperature Sensitive Memory	§	Assignee:	Intel Corporation
	§		

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO PAPER NO. 20041208

Sir:

In response to the office action mailed December 14, 2004, reconsideration is requested in view of the following remarks.

Date of Deposit: February 25, 2005
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden
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